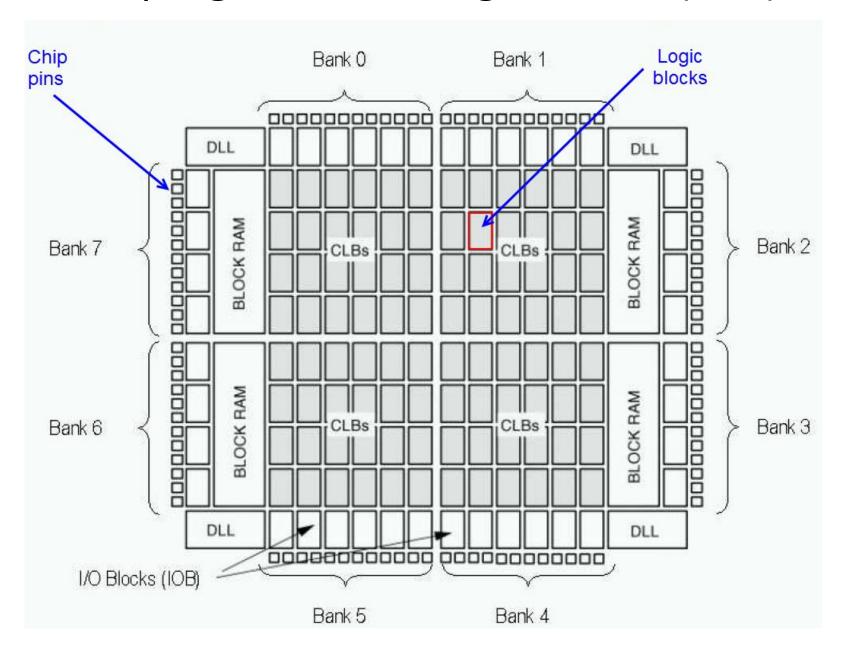
Example of a programmable logic device (PLD)



Logic blocks to implement functions

CPLD (macrocells)

FPGA (Look-up table LUT)

