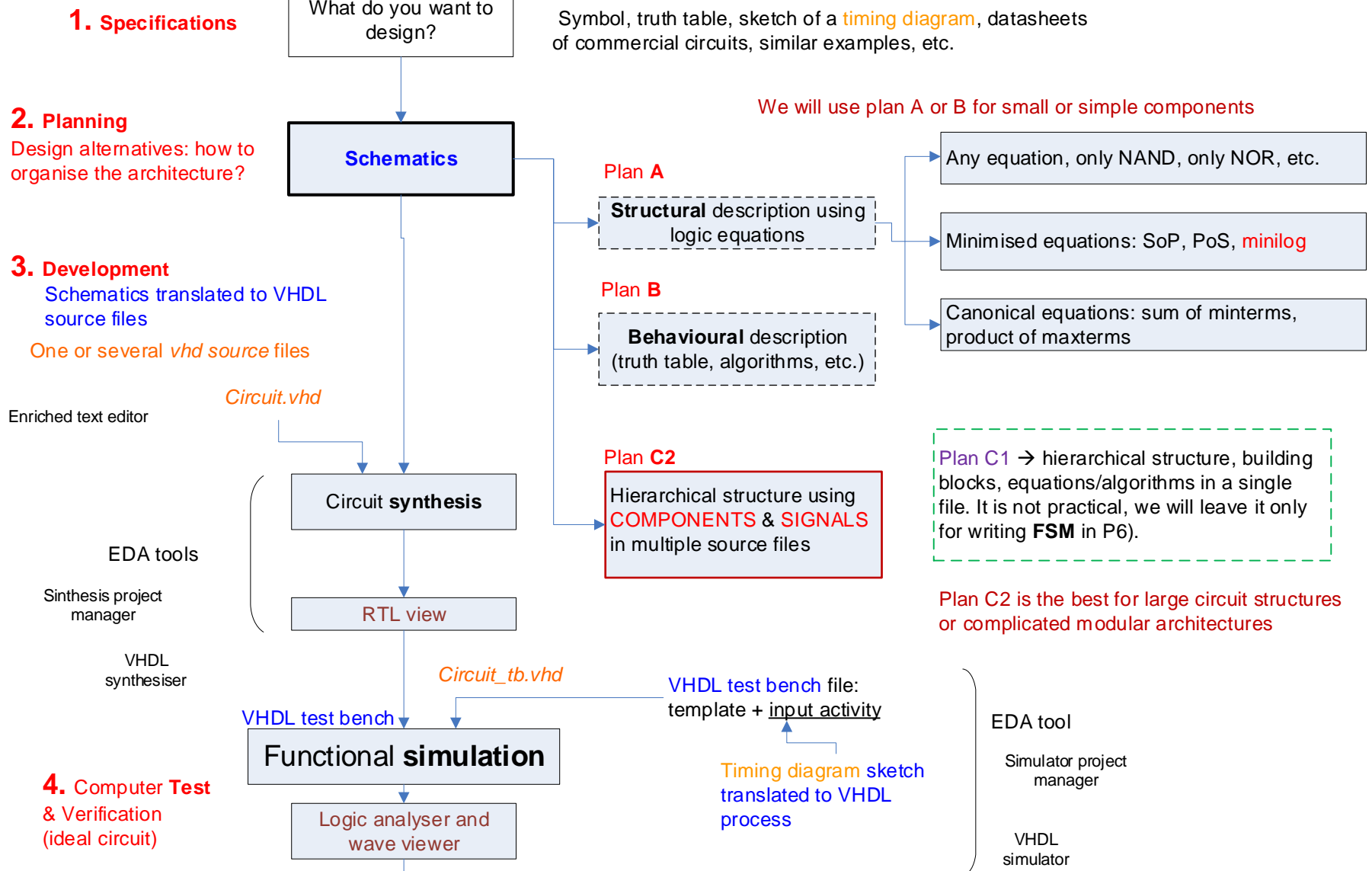


CSD design flow for digital circuits using EDA tools and VHDL

Part 1: Designing and testing an ideal circuit



Part 2: Transforming your ideal circuit in a real circuit configured into a programmable logic device (PLD) with technology CPLD or FPGA

